

JNTU ONLINE EXAMINATIONS [Mid 2 - Compiler Design]

1. Uniform symbol table

- a. Contains all constants in program
 - b. Is a permanent table of decision rules in the form of patterns for matching with the uniform symbol table to discover syntactic structure
 - c. **Consists of full or partial list of tokens as they appear in program created by lexical analysis and used for syntax analysis and interpretation**
 - d. A permanent table which lists all key words and special symbols of language in symbolic form
- 2. When inserted the characters of the string: KRPCSNYTJM into hash table of size 10 by using hash function $H(X) = (\text{ORD}(X) - \text{ORD}('A') + 1) \bmod 10$ and linear probing to resolve collisions. Insertions that cause collisions are**
- a. C, S
 - b. Y, M
 - c. **J, M**
 - d. S, T
- 3. A hash function is defined as $F(\text{key}) = \text{key} \bmod 7$ with linear probing is used to insert the keys 37,38,72,48,98,11,56 into a hash table indexed from 0 to 6. Then in which location the key 11 is stored.**
- a. 6
 - b. **5**
 - c. 1
 - d. 4
- 4. A hash function is defined as $F(\text{key}) = \text{key} \bmod 7$ with linear probing is used to insert the keys 37,38,72,48,98,11,56 into a hash table indexed from 0 to 6. Then following key is stored as the last element**
- a. 98
 - b. 56
 - c. 11
 - d. **48**
- 5. The storage strategy in which activation record is maintained even after the execution of a procedure is completed is**
- a. Stack allocation
 - b. **Heap allocation**
 - c. Static allocation
 - d. Dynamic allocation
- 6. The average search length of the table in the linear list of 'n' records for the look up operation requires**
- a.
 - b.
 - c.
 - d.
- 7. In the search trees implementation the expected time required to enter 'n' names and make 'm' enquiries is proportional to**
- a. **$(n + m) \log n$**
 - b. $(m + n) \log m$
 - c. $m n \log n$
 - d. $m n \log m$
- 8. Time is independent of number of entries in a symbol table among**
- a. Linear list
 - b. Search tree
 - c. **Hash table**

- d. Self-organizing lists
9. Based on the property of locality of reference the symbol table is implemented in
- Linear list
 - search tree
 - hash table**
 - self-organizing list
10. Access time of symbol table will be logarithmic if it is implemented by
- linear list
 - search tree**
 - hash table
 - self-organizing list
11. The following symbol table implementation has the minimum access time.
- hash table**
 - search tree
 - linear list
 - self-organizing list
12. A data structure which is used to store information about various source languages constructs is
- parse tree
 - synthesized grammar
 - symbol table**
 - derivation tree
13. The names referenced frequently will be at the front among the following implementation of the symbol table
- search trees
 - hash tables
 - self-organizing lists**
 - linear lists
14. Requirement of storage for an organization using the variable length entries to the fixed length entries is
- less**
 - more
 - equal
 - can't compare
15. In linked list, to insert 'n' names and 'm' enquiries in the symbol table, the total work is
- $c n m$
 - $m + n$
 - $c n (n + m)$**
 - $c m (n + m)$
16. On an average, the time required per operation to insert 'n' names and 'm' enquiries in the symbol table by hashing requires
- $c n (n + m)$
 - $c m (n + m)$
 - constant**
 - no
17. The following allocation can become stack allocation by using relative address for storage in activation records
- Static**
 - Heap
 - Dynamic
 - Hash
18. The allocation strategy in which only one occurrence of each object is allowable at a given moment during program execution is
- Stack
 - Static**
 - Register

- d. Heap
19. During compile time it is impossible to determine the number of times recursive procedure is going to be involved in the following allocation strategy
- Stack
 - Static**
 - Register
 - Heap
20. The allocation in which the activation record is fixed at compile time in memory is
- Static**
 - Dynamic
 - Heap
 - Stack
21. The following is not possible in static storage schema
- Fixed length strings
 - Nested procedures**
 - Switch statements
 - Structures
22. The allocation strategy in which if names are bounded, then there is no need for a runtime support package is
- Stack
 - Static**
 - Register
 - Heap
23. The allocation which manages storage for all data objects at compile time is
- Dynamic
 - Static**
 - Heap
 - Stack
24. The allocation strategy in which the size of each object must be known at compile time is
- Static**
 - Stack
 - Register
 - Heap
25. FORTAN has the following storage allocation strategy
- Stack
 - Static**
 - Heap
 - Register
26. In the following memory allocation program variables remain permanently allocated irrespective of their accessibility at any stage of programs execution.
- Stack
 - Static**
 - Heap
 - Register
27. Data structures cannot be created dynamically in the following allocation
- Static**
 - Stack
 - Heap
 - Register
28. Language that does not support dynamic allocation is
- ALGOL
 - FORTAN**
 - PASCAL
 - PL/I
29. Dynamic allocation of storage areas with VSAM files is accomplished by

- a. Hashing
 - b. Control splits**
 - c. Overflow areas
 - d. Relative recording
- 30. The following allocation can become stack allocation by using relative address for storage in activation records.**
- a. Static**
 - b. Heap
 - c. Dynamic
 - d. Hash
- 31. The allocation in which the array base address is not known at compile time.**
- a. Static
 - b. Dynamic**
 - c. Register
 - d. Paged
- 32. The storage allocation followed for Strings in SNOBOL is.**
- a. Stack
 - b. Heap**
 - c. Register
 - d. Static
- 33. In stack allocation a new activation record is pushed into the stack for each exception of**
- a. Data base
 - b. Assignment statement
 - c. Procedure**
 - d. MACRO
- 34. When the procedure ends in stack allocation the record is**
- a. Pushed
 - b. Popped**
 - c. Peeped
 - d. Displayed
- 35. When there is reference to storage that has been allocated, then this may occur**
- a. Dangling**
 - b. Indexed
 - c. Offset
 - d. Virtual
- 36. The allocation which is useful for implementing data whose size varies as the program is running is**
- a. Stacks
 - b. Static
 - c. Heap**
 - d. Register
- 37. The following manages runtime storage as stack**
- a. Static
 - b. Dynamic
 - c. Heap
 - d. Stack**
- 38. The allocation strategy which allocates and deallocates storage as needed at runtime from heap data area is**
- a. Static
 - b. Stack
 - c. Heap**
 - d. Register
- 39. Heap allocation is required for languages that**
- a. support recursion
 - b. support dynamic data structure**
 - c. use dynamic scope rules

d. use dynamic programming
40. In the following allocation words in the activation record can be accessed as offsets from the value in the register.

- a. **Static**
- b. Dynamic
- c. Heap
- d. Stack

41. Storage for names local to the procedure appears in

- a. **Activation record**
- b. Symbol table
- c. Hash table
- d. Process table

42. The call statement in the intermediate code is implemented by

- a. MOV
- b. GOTO
- c. **MOV and GOTO**
- d. HALT

43. The allocation which is useful for handling recursive procedure is

- a. **Stack**
- b. Heap
- c. Static
- d. Register

44. A record in PASCAL is defined by
type rec = record

```
----- n:integer;  
----- case(var1,var2) of  
----- var1:(x,y:integer);  
----- var2:(p,q:real);  
----- end;  
end;
```

Suppose an array of 100 such records was declared as a machine which uses 4 bytes for an integer and 8 bytes for a real. How much space would the compiler has to reserve for an array?

- a. 1600
- b. 1800
- c. **2000**
- d. 2020

45. The program fragment that follows is written in a block structured language .Assuming that it is syntactically correct and determine its output

```
begin  
----- integer x,y;  
----- x:=3;  
----- y:=7;  
begin  
----- integer x;  
begin  
----- integer y;  
----- y:=9;  
----- x:=2*y;  
end;  
----- x:=x+y;  
----- print(x);  
end;  
----- print(x);  
end.
```

- a. 3 - - - 25
- b. **25 - - - 3**
- c. 3 - - - 3
- d. 25 - - - 25

46. Assuming 32 bit 2's complement representation for integers and 8-bit ASCII representation for characters, how many bytes of memory are occupied by the following Pascal declaration

```
var rec: RECORD
----- F1:RECORD
----- part1:RECORD
----- p1:char;
----- p2:integer;
----- END;
----- part2 :char;
F2: array [1..2] of RECORD
----- x1: integer;
----- x2: array [1..3] of char;
----- END;
----- END;
Total allocated memory is
```

- a. 10
- b. **20**
- c. 30
- d. 40

47. struct

```
{
short s[5];
union{
float y;
long z;
} u;
}t;
```

Assume short, float, long occupy 2 bytes,4 bytes,8 bytes respectively .The storage for variable 't' ignoring alignment is

- a. 22 bytes
- b. 14 bytes
- c. **18 bytes**
- d. 10 bytes

48. The output of following program in a language that has dynamic scoping is:

```
var x:real;
procedure s1;
begin
////////// print(x);
end;
procedure s2;
var x:real;
begin
x:=0.125;
s1;
end;
begin
x:=0.25;
s1;
s2;
end;
```

- a. 0.125 -- 0.125
- b. **0.25 -- 0.125**
- c. 0.25 -- 0.25
- d. 0.125 -- 0.25

The program fragment that follows is written in a block structured language Assuming that it is syntactically correct and determine its output

```
union{
struct
{
```

```
char a,b,c,d;  
}s1; www.PRsolutions.in
```

49. struct

```
{  
int i ,j;  
short s;  
} s2;  
long z;  
float f;  
double m;  
} u;  
The size of u is
```

- a. 8 bytes
- b. 4 bytes**
- c. 2 bytes
- d. 16 bytes

50. If the language has dynamic scoping and parameters are passed by reference, then what will be printed by the following?

```
p( )  
var n:int;  
procedure w (var x:int)  
begin  
x=x+1;  
print(x);  
end;  
procedure D( )  
begin  
var x:int;  
x=3;  
w(n);  
end;  
begin /* main program*/  
n=10;  
D;  
end.
```

- a. 10
- b. 11
- c. 3
- d. 4**

51. Consider the following variant record declaration in Pascal

```
Type abc =RECORD  
----- x:integer  
----- case(y:integer) of  
----- 1:(m:integer,n:real);  
----- 2:(e,f:integer);  
END;
```

suppose a program uses an array of p such records .Integer needs 2 bytes of storage and real needs 4 bytes of storage. If an array occupies 480 bytes, the value of p is

- a. 10
- b. 64
- c. 60**
- d. 80

52. A region of validity every name possesses in the source program is

- a. Scope**
- b. Life time
- c. Binding
- d. Domain

53. A following for the procedure is a number that is obtained by standing with a value of one for the main and adding one to it every time we go from an enclosing to enclosed procedure.

- a. Nesting loop
 - b. Nesting depth**
 - c. line numbering
 - d. Recursion
54. Consider the following block of code
- ```
p()
begin
x=10, y=3;
func (y,x,x);
print (x,y);
end;
func (x,y,z)
begin
y=y+4;
z=x+y+z;
end;
```
- If parameters are passed by reference ,the output of above procedure is
- a. 31 - - 3
  - b. 3 - - 31
  - c. 3 - - 3
  - d. 31 - - 31
55. In analyzing the compilation of a program 'machine independent optimization' is associated with
- a. Recognition of basic syntactic construction through reduction
  - b. Recognition of basic elements and creation of uniform symbols
  - c. Creation of more optimal matrix**
  - d. Use of macro processor to produce more optimal assembly code
56. The following is used as the key to accessing the scope information from the symbol table
- a. Procedure name
  - b. Procedure name, nesting depth**
  - c. Nesting depth
  - d. Usage count
57. The following of a procedure is a number that is obtained by standing with a value of one for the main and adding one to it every time we go from an enclosing to enclosed procedure
- a. Nesting loop
  - b. Nesting depth**
  - c. Nesting breadth
  - d. Recursion
58. Generation of intermediate code based on a abstract machine model is useful in Compilers because
- a. It makes implementation of lexical analysis and syntax analysis easier**
  - b. Syntax directed translations can be written for intermediate code generation
  - c. It enhances the portability of front end of compiler
  - d. It is not possible to generate code for real machines directly from high level Language programs
59. An optimized compiler
- a. Is optimized to occupy less space
  - b. Is optimized to take less time for execution
  - c. Optimized the code**
  - d. Optimized to small typing font
60. The following refers to the techniques a compiler can employ in an attempt to produce a better object language program than the most obvious for a given source program is
- a. Code generation
  - b. Code optimization**
  - c. Code execution
  - d. Code debugging
61. The "90-10" rule states that

- a. 90 % of code is executed in 10 % of time
  - b. 90 % of time is spent in 10 % of code**
  - c. 90 % of time is spent in correcting 10 % of errors
  - d. 10 % of time is spent in correcting 90 % of errors
- 62. The most heavily travelled parts of programs are**
- a. Inner loops**
  - b. Constants
  - c. Static variables
  - d. Global variables
- 63. Machine independent code optimization can be applied to**
- a. Source code
  - b. Intermediate representation**
  - c. Object code
  - d. Run-time output
- 64. At a point in a program if the value of variable can be used subsequently, then that variable is**
- a. Live**
  - b. Dead
  - c. Duplicate
  - d. Aliasing
- 65. The region of validity every name possesses in the source program is**
- a. Scope**
  - b. Life time
  - c. Binding
  - d. Domain
- 66. Which of the following is not a peephole optimization?**
- a. Removal of unreachable code
  - b. Elimination of multiple groups
  - c. Elimination of loop invariant compilation**
  - d. Loop unrolling
- 67. At point p if no matter what path is taken from p, the expression e will be evaluated before any of its operands are defined, then expression e is said to be**
- a. Lazy
  - b. Late
  - c. Busy**
  - d. Dummy
- 68. The evaluation which avoids the action at all which is clearly advantageous**
- a. Lat
  - b. Lazy**
  - c. Critical
  - d. Smart
- 69. Which of the following comments about peep hole optimization are false.**
- a. It is applied to small part of the code
  - b. It can be used to optimize intermediate code
  - c. To get the best out of this, it has to be applied repeatedly
  - d. It can be applied to the code that is contiguous**
- 70. The following examines short sequences of code and determines a sequence can be replaced by a shorter equivalent sequence.**
- a. Peep hole optimizer**
  - b. Assembler
  - c. Linker
  - d. Loader
- 71. The evaluation ordering in which we know beforehand that will have to perform the action anyway, but we find it advantageous to perform it as late as possible**
- a. Late**
  - b. Lazy
  - c. Critical

- d. Smart
72. The evaluation ordering in which, code for node is issued as soon as the code for all of its Operands has been issued
- Early
  - Late
  - Lazy
  - Critical
73. Peep-hole optimization is form of
- Local optimization
  - Constant folding
  - Copy propagation
  - Data flow analysis
74. If the transformation of a program can be performed by looking only at statements in basic block then it is said to be
- Local
  - Global
  - Algebraic
  - Matrix
75. If E was previously computed and the values of variable in E have not changed since previous computation ,then an occurrence of an expression E is
- Copy propagation
  - Dead code
  - Common sub expressions
  - Constant folding
76. In block B if s occurs in B and there is no subsequent assignment to y within B, then the copy statement  $s: x := y$  is
- Generated
  - Killed
  - Blocked
  - Dead
77. In block B if x or y is assigned there and s is not in B then  $s: x := y$  is
- Generated
  - Killed
  - Blocked
  - Dead
78. If two or more expressions denote same memory address, then expressions are
- Aliases
  - Definitions
  - Superior
  - Inferior
79. Operations that can be removed completely are called
- Strength reduction
  - Null sequences
  - Arithmetic simplification
  - Constant folding
80. Given the following code
- ```
X=A+B;
```

```
Y=A+B;
```

And the corresponding optimized code as

```
----- Z=A+B;
```

```
----- X=Z;
```

```
----- 100 Y=Z;
```

When will be optimized code pose a problem?

- Z may not remain same after the control reaches here first time since it is labeled

- b. When Z is undefined
 - c. When memory is consideration
 - d. Doesn't pose a problem
- 81. Can the loop invariant $X = A - B$ from the following code be moved out**
For I = 1 to 10
A = B * C;
X = A - B;
- a. **Yes**
 - b. No
 - c. $X=A-B$ is not invariant
 - d. Wrong code
- 82. The following transformation takes an expression that yields the same result independent of the number of times.**
- a. Reduction in strength
 - b. Induction variable
 - c. Constant folding
 - d. **Code motion**
- 83. The optimization that avoids a test at each iteration is**
- a. **Loop unrolling**
 - b. Loop ramming
 - c. Loop nesting
 - d. Loop scrolling
- 84. The method that merges the bodies of two loops is**
- a. Loop nesting
 - b. Constant folding
 - c. **Loop ramming**
 - d. Loop unrolling
- 85. The code for repetition can be optimized by**
- a. **Loop unrolling**
 - b. Loop jamming
 - c. Nested loop
 - d. Code motion
- 86. The following can not be used to identify loops**
- a. Depth first ordering
 - b. Reducible graphs
 - c. Dominators
 - d. **Flow chart**
- 87. Replacement of a string concatenation operator "||" by an addition is example for**
- a. **Strength reduction**
 - b. Operator overriding
 - c. Operator over loading
 - d. Operator elimination
- 88. The replacement of an expensive operation by a cheaper one**
- a. Operator overriding
 - b. Operator overloading
 - c. **Reduction in strength**
 - d. Operator elimination
- 89. The loop which does not contain any other loops are**
- a. Natural loop
 - b. **Inner loop**
 - c. Main loop
 - d. Nested loop
- 90. Identify the basic blocks in the following code**
10 goto 20
20 goto 10
- a. Go to 20
 - b. Go to 10

- c. **Two independent basic blocks**
 - d. Both statements in one block
- 91. Loop is collection of nodes that**
- a. Is loosely connected
 - b. Is strongly connected with several entries
 - c. Is having several entries
 - d. **Is strongly connected with unique entry**
- 92. In loop optimization technique a loop whose body is rarely executed is**
- a. Dead code
 - b. Blank code
 - c. Redundant code
 - d. **Blank stripper**
- 93. If the following optimization is attempted, count of number of jumps to each label can be found from symbol table of that label**
- a. Back tracking
 - b. **Peephole optimization**
 - c. Dynamic programming
 - d. Global optimization
- 94. The use of the following greatly improves the code when pushing or popping a stack, as in parameter passing**
- a. Flow of control
 - b. Auto increment addressing modes
 - c. Auto decrement addressing modes
 - d. **Both auto increment and auto decrement addressing modes**
- 95. The following can be done through peephole optimization**
- a. Dynamic programming
 - b. Greedy method
 - c. **Unreachable code elimination**
 - d. Code generation
- 96. The following algebraic identities can be eliminated through peephole optimization**
- a. $x:=x+0$
 - b. $x:=x*1$
 - c. **$x:=x+0$ and $x:=x*1$**
 - d. Jumps over jumps
- 97. Replacing the exponent operator by the shift of multiplication is**
- a. Constant folding
 - b. **Reduction in strength**
 - c. Copy propagation
 - d. Elimination of dead variables
- 98. Method to improve the target program by examining a short sequence of target instructions**
- a. Back tracking
 - b. **Peephole optimization**
 - c. Dynamic programming
 - d. Global optimization
- 99. Program transformation that is not characteristic of peephole optimization**
- a. Redundant instruction elimination
 - b. Flow of control optimization
 - c. **Back patching**
 - d. Use of machine idioms
- 100. The following is the small moving window in target program**
- a. Screen saver
 - b. Graphics
 - c. **Peephole**
 - d. Greedy window
- 101. Use of machine idioms is one of the characteristic of**

- a. Back tracking
 - b. Peephole optimization**
 - c. Dynamic programming
 - d. Global optimization
- 102. Peephole optimization is to eliminate**
- a. Constant folding
 - b. Jumps over jumps**
 - c. Copy propagation
 - d. Elimination of dead variables
- 103. Machine independent optimization is**
- a. Register allocation
 - b. Frequency reduction
 - c. Data intermixed with instructions**
 - d. Machine features instructions
- 104. Computing two or more identical computations to a place in the program where the computation can be done once and the result to used in the entire original places is**
- a. Replacing
 - b. Profiling**
 - c. Hoisting
 - d. Rearranging
- 105. Deducing at compile time that the value of an expression is a constant and using that constant instead is**
- a. Constant folding**
 - b. Constant deduction
 - c. Macro substitution
 - d. Copy propagation
- 106. The following computation is done and placed the expression before the loop in the transformation of code motion**
- a. Loop-invariant**
 - b. Overloading
 - c. Overriding
 - d. Constant folding
- 107. Changing the form to preserve the program semantics is**
- a. Coding
 - b. Frequency reduction**
 - c. Constant folding
 - d. Constant substitution
- 108. Replacing expressions by their value if the value can be computed at compile time is called**
- a. Constant substitution
 - b. Constant folding**
 - c. Macro
 - d. Infix expression
- 109. Replacing the expression $2*3.14$ by 6.28 is**
- a. Constant folding**
 - b. Induction variable
 - c. Strength reduction
 - d. Code reduction
- 110. An estimate of how frequently a variable used is**
- a. Usage count**
 - b. Reference count
 - c. Program count
 - d. Process count
- 111. The modification that decreases the amount of code in a loop is**
- a. Constant folding
 - b. Constant reduction

- c. **Code motion**
- d. Reduction strength
- 112. **Movement of the code from inside to outside is**
 - a. Coding
 - b. **Frequency reduction**
 - c. Constant folding
 - d. Constant substitution
- 113. **In DAG the interior nodes are labeled with**
 - a. Numbers in DFS
 - b. Numbers in BFS
 - c. **Identifiers**
 - d. Special colors
- 114. **Sorting techniques used for evaluation of interior nodes of DAG in any order is**
 - a. Quick sort
 - b. Selection sort
 - c. Merge sort
 - d. **Topological sort**
- 115. **If every path from the initial node goes through that particular node, then the node is said**
 - a. Header
 - b. **Dominator**
 - c. Parent
 - d. Descendant
- 116. **DAG has**
 - a. Only one root
 - b. **Any number of roots**
 - c. No root
 - d. No nodes at all
- 117. **A basic block can be analyzed by**
 - a. Flow graph
 - b. Flow chart
 - c. **DAG**
 - d. RAG
- 118. **In DAG, interior nodes are labeled by**
 - a. **Operator symbol**
 - b. Unique identifiers
 - c. Operands
 - d. Node numbers
- 119. **Common sub expressions can be detected automatically by using**
 - a. Flow graph
 - b. Flow chart
 - c. **DAG**
 - d. RAG
- 120. **Date structures useful for implementing transformations on basic blocks are**
 - a. RAG
 - b. **DAG**
 - c. Gaunt chart
 - d. B⁺ tree
- 121. **DAG representation of a basic block allows**
 - a. **Automatic detection of local common sub expression**
 - b. Automatic detection of induction variables
 - c. Automatic detection of loop invariables
 - d. Automatic motion to code
- 122. **In DAG, labeling of nodes are**
 - a. Mandatory
 - b. **Optional**

- c. Compulsory
 - d. Automatically generated
- 123. The statement that may be safely removed without changing the value of basic block is**
- a. Live code
 - b. Dead code**
 - c. Temporary variables
 - d. Common sub-expression
- 124. The block which can be transformed into an equivalent block in which each statement that defines a temporary defines a new temporary is**
- a. Normal form**
 - b. Temporary block
 - c. Procedural block
 - d. Null block
- 125. The following basic block permits all statement interchanges that are possible**
- a. Normal form**
 - b. Dead block
 - c. Temporary block
 - d. Null block
- 126. Determine the number of definition and reference points of variable A in the following code**
- ```
A = Z; /* statement S0*/
A = B; /* statement S1*/
C = D; /* statement S2*/
C = C + A;
```
- a. 1,3
  - b. 2,3
  - c. 0,4
  - d. Can't be determined
- 127. Determine the definition points d; affecting the expression C = C+A in the following code**

```
A=Z; /* statement S0*/
A=B; /* statement S1*/
C=D; /* statement S2*/
C=C+A;
```

  - a. Statement S0 and S1
  - b. Statement S1 and S2**
  - c. Statement S0 and S2
  - d. Undetermined

**128. Structure preserving transformations on basic blocks are**

  - a. Dead code elimination**
  - b. Strength reduction
  - c. Copy propagation
  - d. Constant folding

**129. In reducible flow graph, the following edges consists only of edges whose heads dominate their tails.**

  - a. Forward edges
  - b. Back edges**
  - c. Pre-header
  - d. Header

**130. Determine the pre-dominant block of block B2 in the program flow graph from the following code**

```
../* Block B0*/
10 go to 100 /* Block B1*/
100 go to 10 /* Block B2*/
```

  - a. B0
  - b. B1
  - c. B2

- d. **Code insufficient**
131. **A sequence of consecutive statements in which the flow of control enters at the beginning and leaves at the end without halt or possibility of branching except at the end is**
- Flow graph
  - DAG
  - Basic block**
  - Loop
132. **The first statement in basic block is**
- Leader**
  - Header
  - Main statement
  - Follow
133. **Any statement that immediately follows the following statement is a leader**
- First statement
  - GOTO or conditional GOTO**
  - While loop
  - Array declaration
134. **The following among these is not structure preserving transformations on basic blocks**
- Common sub-expression elimination
  - Invariant variables elimination**
  - Dead code elimination
  - Interchanging of two independent adjacent statements
135. **Determine the path(s) in the program flow graph for the code**  
**10 goto 100 /\* Block B1\*/**  
**100 goto 10 /\* Block B2\*/**
- (B1, B2)
  - (B2,B1)
  - (B1,B2,B1)**
  - No path
136. **The graph that shows the basic blocks and their success of relationship is called**
- DAG
  - Flow graph**
  - Control graph
  - Hamilton graph
137. **Which of the following class of statement usually produces no executable code when compiled.**
- Declaration
  - Assignment
  - Input and output
  - Structural statements**
138. **Edges in the flow graph whose heads dominate their tails are called**
- Back edges**
  - Front edges
  - Flow edges
  - Head edges
139. **Ambiguous definitions of x are**
- An assignment through a pointer that could refer x**
  - Assignment to x
  - Storing value in x
  - Reading a value from an I/O device
140. **The reaching definition information is stored in**
- Activation record
  - Symbol table
  - Ud-chains**

- d. Du-chains
- 141. Data flow equation has the form**
- a.  $OUT[S] = GEN[S] \cap ( IN [S] + KILL [S] )$
  - b.  $OUT[S] = GEN[S] \cap ( IN [S] - KILL [S] )$
  - c.  $OUT[S] = GEN[S]$

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